

## **IN THE CLAIMS**

### **Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application.

### **Listing of Claims:**

1.(Currently amended)      An arrangement of several resistors comprising: the resistors jointly positioned in one and the same well of a semiconductor device, wherein each of the resistors are extending in a longitudinal direction and are spaced laterally from each other and are wherein each of the resistors is alternately displaced toward opposite sides of the well in the longitudinal direction of the resistors in relation to the an adjacent resistor, the arrangement configured to reduce coupling between said resistors.

2. (Original)    The arrangement according to claim 1, wherein the resistors all have substantially the same length.

3. (Original)    The arrangement according to claim 1, wherein the resistors all have substantially the same breadth or width, respectively.

4. (Original)    The arrangement according to claim 1, wherein the resistors all have substantially the same depth.

5. (Original)    The arrangement according to claim 1, wherein the resistors all are of substantially identical structure.

6. (Original)    The arrangement according to claim 1, wherein the resistors all have substantially the same individual resistance value.

7. (Cancelled)

8. (Previously presented)      The arrangement according to claim 1, said arrangement comprising at least five resistors.

9. (Cancelled) .

10. (Original) The arrangement according to claim 1, wherein a first resistor of said resistors is displaced approximately a length of a second resistor of said resistors, wherein said first resistor is adjacent to said second resistor.

11. (Currently amended) The arrangement according to claim 1, wherein a distance between a first resistor of said resistors and a second resistor of said resistors, when viewed in a transverse direction of the resistors, is smaller than one third of a breadth or width, ~~of a length~~ of said first resistor or of said second resistor, respectively.

12. (Currently amended) The arrangement according to claim 1, wherein a distance between a first resistor of said resistors and a second resistor of said resistors, when viewed in a transverse direction of the resistors, is smaller than one fifth of a breadth or width ~~of a length~~ of said first resistor or of said second resistor, respectively.

13. (Previously presented) The arrangement according to claim 1, wherein the well is relatively weakly n-doped.

14. (Previously presented) The arrangement according to claim 1, wherein the resistors are relatively strongly n-doped.

15. (Original) The arrangement according to claim 14, wherein the resistors are n-diffusion resistors.

16. (Original) The arrangement according to claim 1, wherein the resistors are connected to corresponding signal driver devices of the semiconductor device.

17. (Original) The arrangement according to claim 1, wherein the resistors are connected to corresponding output pads of the semiconductor device.

18. (Original) The arrangement according to claim 1, wherein the resistors are connected in parallel.

19. (Original) The arrangement according to claim 18, wherein the resistors are connected in parallel such that a total resistance value results for the resistors connected in parallel which corresponds to a desired resistance value.

20. (Original) The arrangement according to claim 19, wherein the resistors connected in parallel are jointly connected to a particular signal driver device, and wherein the desired resistance value corresponds to a resistance value desired for the corresponding signal driver device.

21. (Currently amended) A semiconductor device comprising: an arrangement of resistors, the arrangement includes the resistors jointly positioned in one and the same well of a semiconductor device, wherein each of the resistors are extending in a longitudinal direction, and are spaced laterally from each other and are wherein each of the resistors is alternately displaced toward opposite sides of the well in the longitudinal direction of the resistors in relation to ~~the~~ an adjacent resistor, the arrangement configured to reduce coupling between said resistors.

22. (Previously presented) The arrangement according to claim 1, wherein a distance between a first resistor of said resistors and a second resistor of said resistors, when viewed in a transverse direction of the resistors, is smaller than one third of a length of said first resistor or of said second resistor, respectively.

23. (Previously presented) The arrangement according to claim 1, wherein a distance between a first resistor of said resistors and a second resistor of said resistors, when viewed in a transverse direction of the resistors, is smaller than one fifth of a length of said first resistor or of said second resistor, respectively.

24. (New) The arrangement according to claim 1, wherein each of the resistors is displaced in the longitudinal direction of the resistors such that substantially none of a first resistor overlaps with an adjacent resistor.